FIG.1

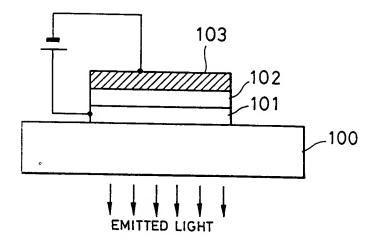


FIG.2

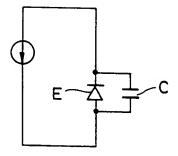
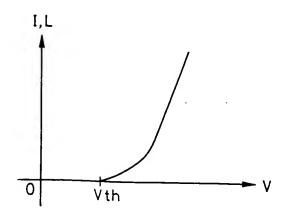


FIG.3



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2mB AmB 6mB~ 汉 EmB,2 EmB,3 EmB,n EmB,1 Amg 6mg~ EmG,2 EmG,3 EmGn EmG,1 7mR AmR 英 6mR~ EmR,3 EmR,n EmR,2 EmR,1 **2**2B A2B 62B ~ E28,2 E2B,3 E2B,n E2B,1 62G~ A2G E2G,n E2G,1 E2G,3 E26,2 62R~ A2R .21B E2R,1 E2R,3 E2R,n E2R,2 618 7 A1B E18,3 E18,n E18,2 61G-J A1G E16,n 5/5/ E16,3 E16,1 E16,2 GIR Y A1R E1R,3 E1R,n E1R,2 3) **B**2 **B**3 В <u>8</u> 2 Vcc 52 Vcc 53 Vcc 51 LIGHT EMISSION CONTROL CIRCUIT

F16.4

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2mB AmB 6mB~ EmB,1 EmB<sub>7</sub>2 EmB,3 EmB,n 7mG 6mG~ Amg EmG,1 EmG,2 EmG,3 EmG,n 7mR AmR ----6mR~ EmR,1 EmR,2 EmR,n EmR,3 **2**2B A2B ·22G 62B ~ E28,2 E2B,1 E28,3 E<sub>2B,n</sub> Azg 62G~ E26,2 E26,3 E<sub>2</sub>G<sub>,n</sub> E2G,1 A2R -218 62R~ E2R,1 E2R,2 E2R,3 E2R,n 618~ A1B -21G E18,1 E18,2 E18,3 E18,n 616~ A1G **21R** E16,1 E16.2 E1G,n E16,3 A1R 61R~ E1R,1 E1R,2 E1R,3 E1R,n 3 **B**2 **B**3 8 찚 2 Vcc 52 Vcc 51 39 Vcc 53 Vcc '5<sub>0</sub> LIGHT EMISSION CONTROL CIRCUIT

F16.5

F1G.6

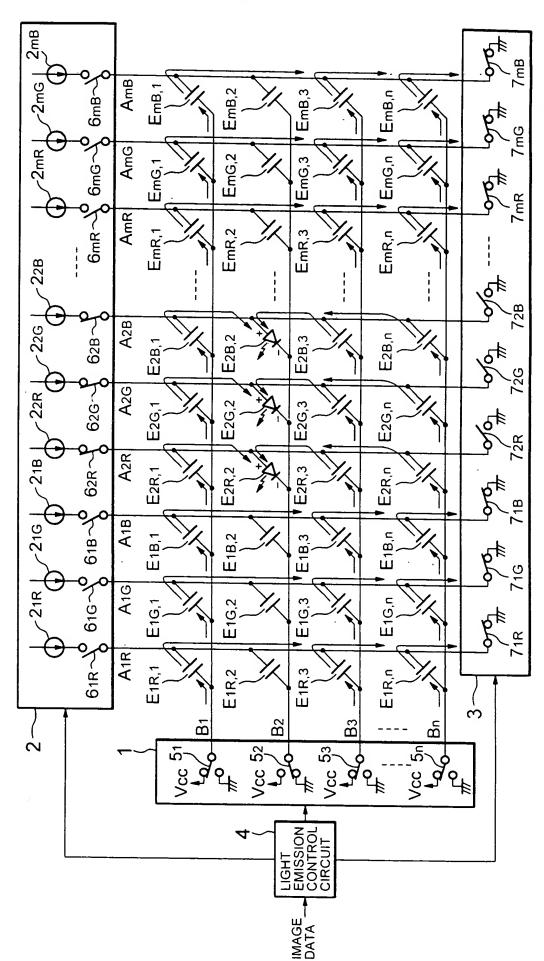
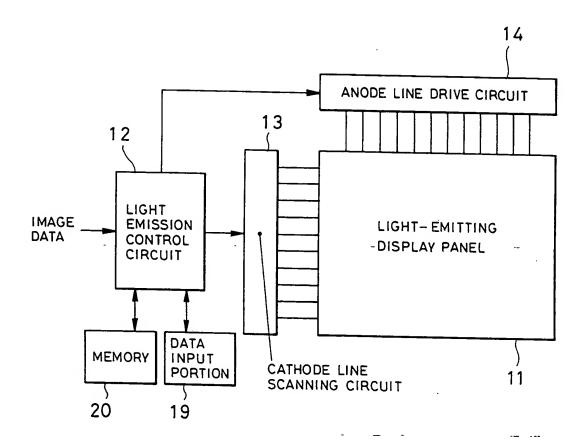


FIG.7



AmB EmB,2 EmB,3 EmB,1 EmB.n Amg EmG,1 EmG,2 EmG,3 EmG,n AmR EmR,1 EmR,2 EmR,3 EmR,n 162B A2B E28,3 E2B,2 E2B,n E28,1 162G A2G E2G,2 E26,1 E26,3 E2G,n 162R A2R E2R,1 E2R,3 E2R,2 E2R.n 161B A1B E18,3 E18,1 E18,2 E18,n 1616 A1G E16,1 E16,3 E16,2 E16,n 161R A1R E1R,1 181R E1R,3 ! E1R,n E1R,2 <u>a</u> **B**2 **B**3 В Vcc152 1/cc151 4 DATA INPUT PORTION <u>ග</u> 12 LIGHT EMISSION CONTROL CIRCUIT 20 MEMORY

F16.8

FIG.9

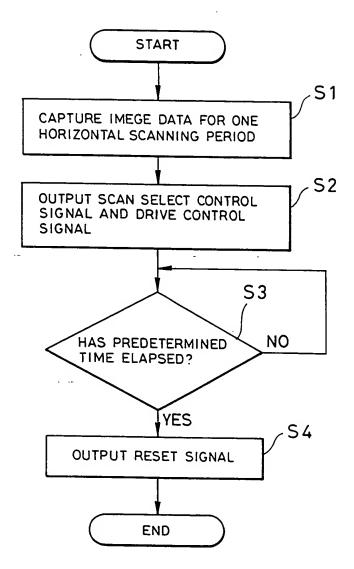
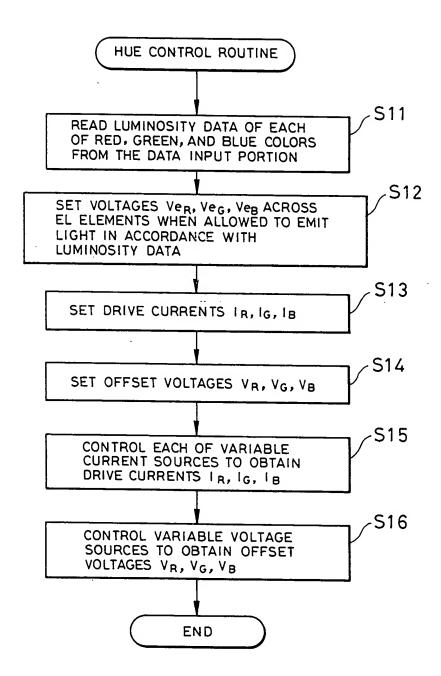
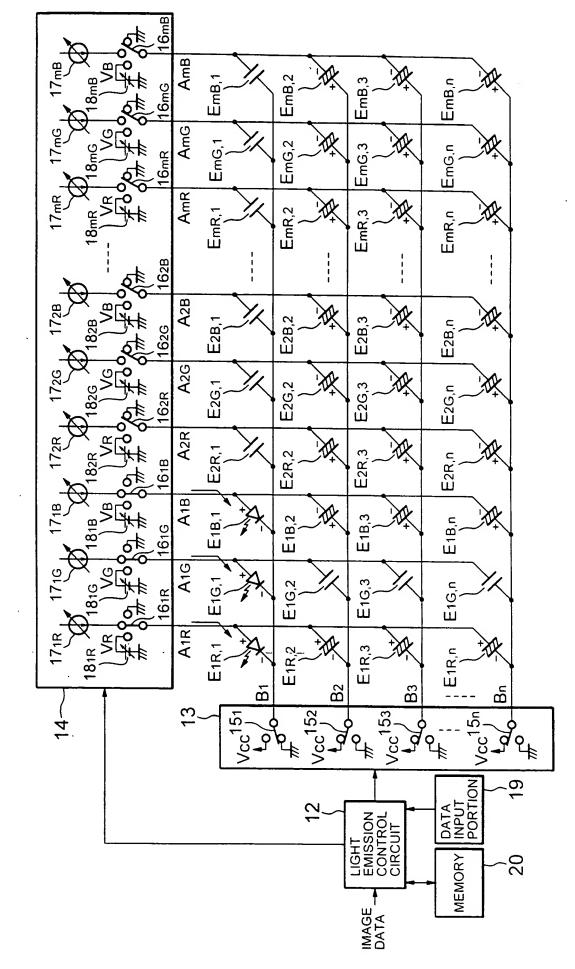


FIG.10





F16.11

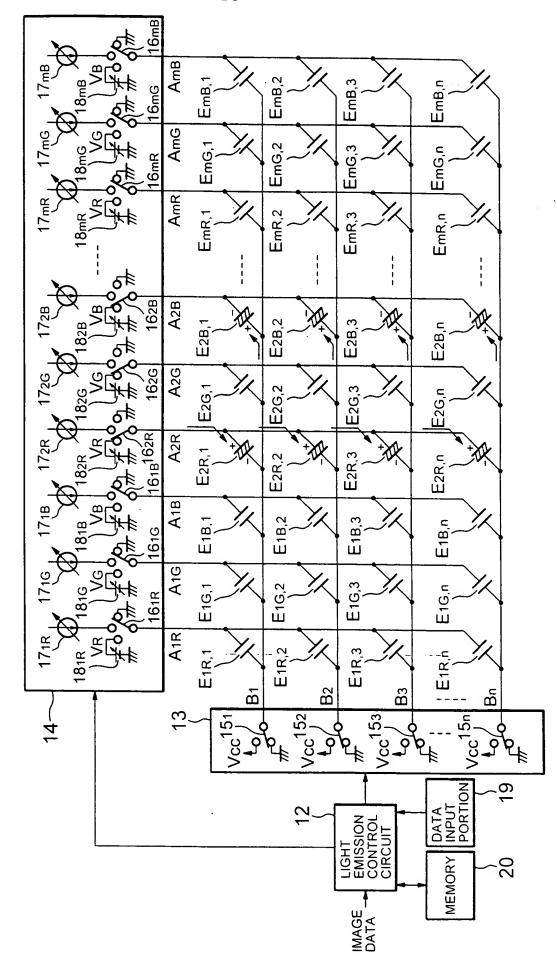


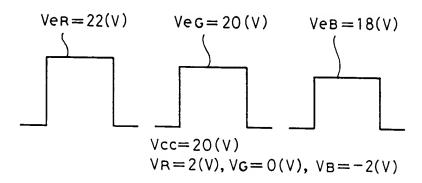
FIG.12

AmG AmR 162B A2B Azg A2R A18 1616 A1G 161R A1R

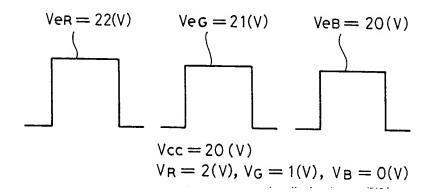
F16.13

AmB EmB,2 EmB,3 EmB,n EmB,1 EmG,2 EmG,1 EmG,3 EmG,n EmR,2 EmR,n EmR,3 EmR,1 E28,2 E28,3 E2B,n E28,1 E26,2 E2G,1 E2G,3 E<sub>2</sub>G<sub>n</sub> E2R,1 E2R,2 E2R,3 E2R,n 狹 E18,2 E18,3 E18,n E18,1 E16,2 E16,n E16,3 E16,1 E1R,1 E1R,2 E1R,3 | E1R,n B<sub>2</sub> . B3 Б <u>m</u> 1Vcc151 DATA INPUT PORTION 19 1,2 LIGHT EMISSION CONTROL CIRCUIT MEMORY 20 IMAGE DATA -

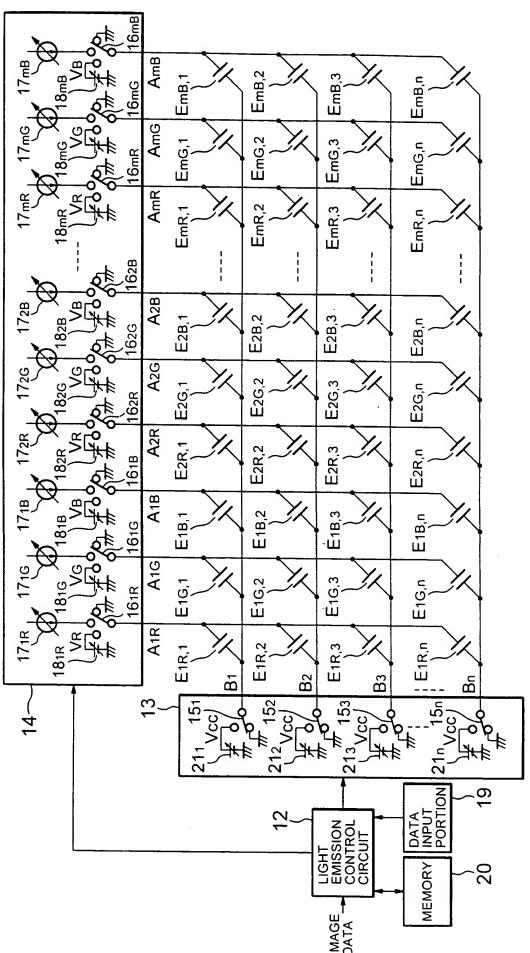
### FIG.14A



### FIG.14B

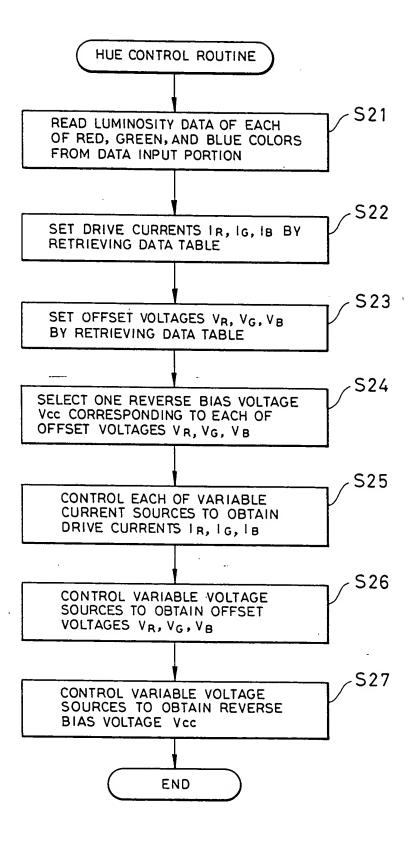


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F16.15

**FIG.16** 



# FIG.17

LEVELS OF HALFTONE		0	1	2	 29	30	31
RED	DRIVE CURRENT	lr0	lr1	Ir2	 Ir29	Ir30	1r31
	OFFSET VOLTAGE	Vr0	Vr1	Vr2	 Vr29	Vr30	Vr31
GREEN	DRIVE CURRENT	Ig 0	lg1	lg 2	 lg 29	lg 3 0	lg31
	OFFSET VOLTAGE	Vg0	Vg1	Vg2	 Vg29	Vg30	Vg31
BLUE	DRIVE CURRENT	Ib0	1Ь1	162	 1629	Ib30	Ib31
	OFFSET VOLTAGE	VPO	V b 1	Vb2	 Vb29	Vb30	Vb31

## FIG.18A

VeR = 25(V)

OFFSET VOLTAGE VR (V)	-5	-4	-3	-2	-1	0	+1	+2	+3
· REVERSE BIAS VOLTAGE Vcc (V)	30	29	28	27	26	25	24	23	22

COMMON VOLTAGE RANGE

### **FIG.18B**

VeG = 21(V)

**************************************									
OFFSET VOLTAGE V <sub>G</sub> (V)	-5	-4	-3	-2	-1	0	+1	+2	+3
REVERSE BIAS VOLTAGE Vcc (V)	26	25	24	23	22	21	20	1.9	18

COMMON VOLTAGE RANGE

### FIG.18C

VeB = 20(V)

OFFSET VOLTAGE VB (V)	<b>-</b> 5	-4	-3	-2	-1	0	+1	+2	+3
REVERSE BIAS VOLTAGE Vcc (V)	25	24	23	22	21	20	19	18	17

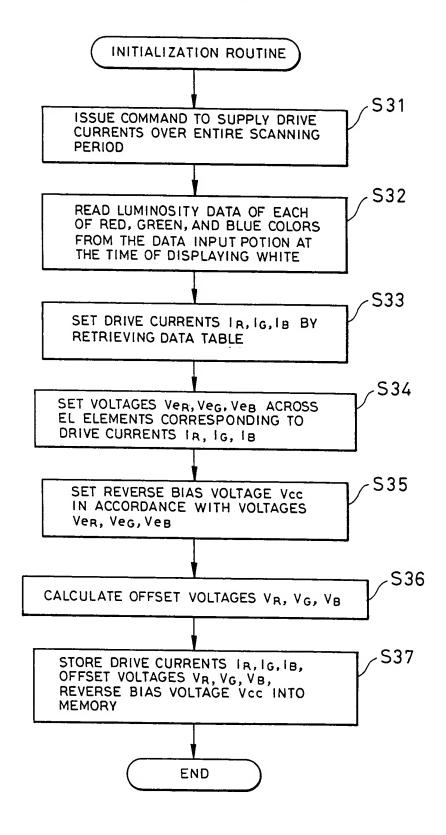
COMMON VOLTAGE RANGE

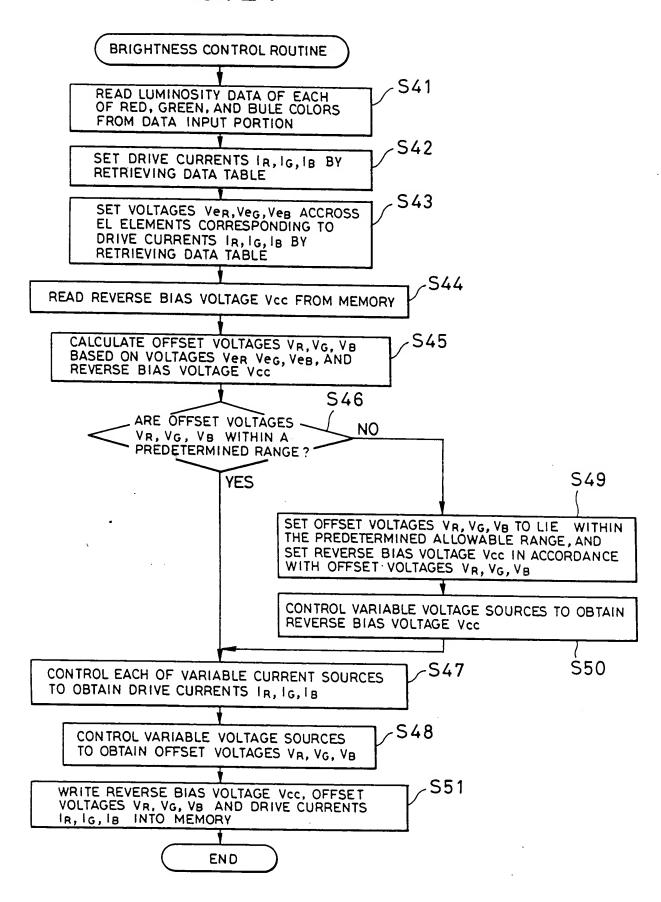
#### **FIG.19**

VeR = 9(V)

OFFSET VOLTAGE VR (V)	-5	-4	-3	-2	-1	0	+1	+2	+3
REVERSE BIAS VOLTAGE Vcc (V)	14	13	12	11	10	9	8	7	6

FIG. 20





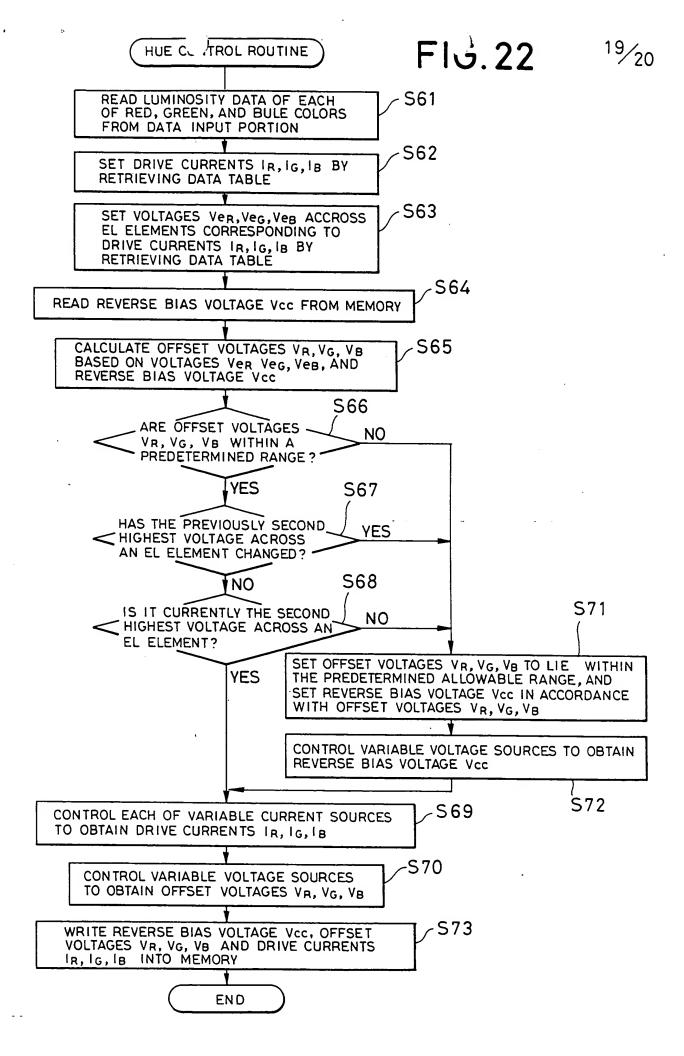


FIG.23

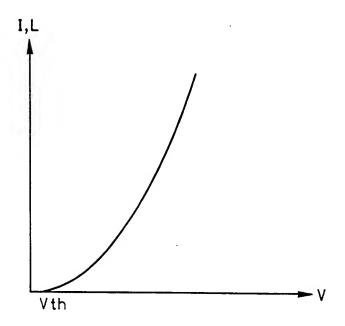


FIG. 24

